

549,678

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
13 January 2005 (13.01.2005)

PCT

(10) International Publication Number
WO 2005/003880 A2

(51) International Patent Classification⁷: **G06F**
(21) International Application Number:
PCT/IL2004/000575
(22) International Filing Date: 29 June 2004 (29.06.2004)
(25) Filing Language: English
(26) Publication Language: English
(30) Priority Data:
60/484,294 3 July 2003 (03.07.2003) US
(71) Applicant (for all designated States except US): **LYCIUM
NETWORKS (B.V.I.) LTD.** [—/IL]; 14 Hatidhar St.,
43000 Raanana (IL).

AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN,
CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI,
GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE,
KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD,
MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG,
PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM,
TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM,
ZW.

(84) Designated States (unless otherwise indicated, for every
kind of regional protection available): ARIPO (BW, GH,
GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM,
ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),
European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI,
FR, GB, GR, HU, IE, IT, LU, MC, NL, PL, PT, RO, SE, SI,
SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ,
GW, ML, MR, NE, SN, TD, TG).

(72) Inventor; and
(75) Inventor/Applicant (for US only): **PELEG, Zohar**
[IL/IL]; 1 Givton Street, 48541 Rosh Haain (IL).

Published:

— without international search report and to be republished
upon receipt of that report

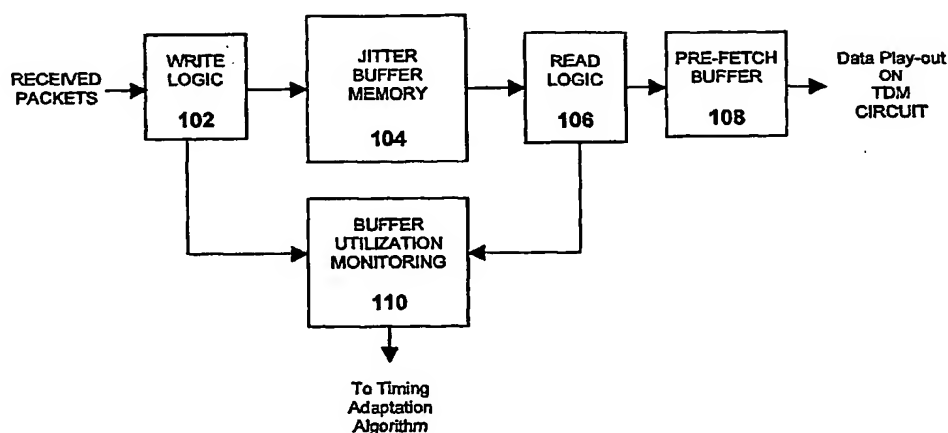
(74) Agent: **FRIEDMAN, Mark**; 7 Jabotnisky St., 52520 Ra-
mat Gan (IL).

For two-letter codes and other abbreviations, refer to the "Guid-
ance Notes on Codes and Abbreviations" appearing at the begin-
ning of each regular issue of the PCT Gazette.

(81) Designated States (unless otherwise indicated, for every
kind of national protection available): AE, AG, AL, AM,

(54) Title: **METHOD AND APPARATUS FOR PARTITIONING ALLOCATION AND MANAGEMENT OF JITTER BUFFER
MEMORY FOR TDM CIRCUIT EMULATION APPLICATIONS**

100



(57) Abstract: A method for partitioning allocation and management of a jitter buffer memory for TDM circuit emulation applica-
tions comprises the steps of obtaining a channel hierarchy for a plurality of packet carrying channels having different channel rates,
obtaining a packet sequential number, and using the channel hierarchy and the packet sequential number, generating a base-address
in the jitter buffer memory. Each channel is allocated a space in the buffer memory that is proportional to its rate, and out-of-order
packets are automatically reordered by the jitter buffer.

WO 2005/003880 A2